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PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. P043D2C3C	SERIAL NUMBER 09/492,982
	APPLICANT(S) FARMWALD ET AL.	
	FILING DATE JANUARY 27, 2000	GROUP ART UNIT 2818

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EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
TNT	4,755,937	July 5, 1989	Glier			
TNT	4,875,192	Oct 17, 1989	Matsumoto			

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

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TNT	Pelgrom et al., "A 32-kbit Variable-Length Shift Register for Digital Audio Application", IEEE Journal of Solid-State Circuits, vol. sc-22, no. 3, June 1987, pp 415-422
	Grover et al., "Precision Time-Transfer in Transport Networks Using Digital Crossconnect Systems", IEEE Paper 47.2 Globecom, 1988, pp 1544-1548
	Gustavson et al., "The Scalable Interface Project (Superbus)" (DRAFT), SCI-22 Aug 88-doc1 pp 1-16, August 22, 1988
	Knut Alnes, "SCI: A Proposal for SCI Operation", SCI-10Nov88-doc23, Norsk Data, Oslo, Norway, pp. 1-12, Nov. 10, 1988
	Knut Alnes, "SCI: A Proposal for SCI Operation", SCI-6Jan89-doc31, Norsk Data, Oslo, Norway, pp. 1-24, Jan 6, 1989
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	Knut Alnes, "Scalable Coherent Interface", SCI-Feb89-doc52, (To appear in Eurobus Conference Proceedings May 1989) pp. 1-8
	Boysel et al., "Four-Phase LSI Logic Offers New Approach to Computer Designer", Four-Phase Systems Inc. Cupertino, CA, Computer Design, April 1970, pp. 141-146,
TNT	Boysel et al., "Random Access MOS Memory Packs More Bits To The Chip", Electronics, Feb. 16, 1970, pp. 109-146,

EXAMINER TNT, NGUYEN	DATE CONSIDERED 05/17/01
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TNT	4,482,999	11/13/84	Janson et al.	370	452	
	4,835,674	05/30/89	Collins et al.	709	214	
	5,193,193	03/09/93	Iyer	710	117	
	5,179,667	01/12/93	Iyer	711	167	
	4,926,385	05/15/90	Fujishima et al.	365	230.03	
	4,566,099	01/21/86	Magerl	370	509	
	4,803,621	02/07/89	Kelly	711	5	
	4,589,108	05/13/86	Billy	370	503	
	4,870,622	09/26/89	Aria et al.	365	230.02	
	5,134,699	07/28/92	Aria et al.	710	35	
	4,878,166	10/31/89	Johnson et al.	710	127	
	4,849,965	07/18/89	Chomel et al.	370	438	
TNT	4,851,990	07/25/89	Johnson et al.	710	100	

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	4,748,617	05/31/88	Drewlo	359	121	
	4,435,762	03/06/84	Milligan et al.	710	6	
	4,839,801	06/13/89	Nicely et al.	710	35	
	4,949,301	08/14/90	Joshi et al.	711	100	
	4,047,246	09/06/77	Kerllenevich et al.	710	61	
	5,029,124	07/02/91	Leahy et al.	710	105	
TNT	4,625,307	11/25/86	Tulpule et al.	370	402	

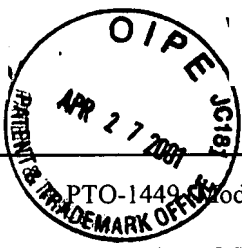
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TNT	Hansen et al., "A RISC MICROPROCESSOR WITH INTEGRAL MMU AND CACHE INTERFACE", MIPS Computer Systems, Sunnyvale, CA, IEEE 1986 pp 145-148
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	Riordan T. "MIPS R2000 Processor Interface 78-00005(C)", MIPS Computer Systems, Sunnyvale, CA, June 30, 1987, pp 1-83
TNT	Moussouris, J. "The Advanced Systems Outlook-Life Beyond RISC: The next 30 years in high-performance computing", Computer Letter, July 31, 1989 (an edited excerpt from an address at the fourth annual conference on the Advanced Systems Outlook, in San Francisco, CA (June 5))

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	5,034,964	Jul. 23, 1991	Khan et al.			

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
TNT	SHO 58-192154	Nov. 9, 1983	Japan			NO
	SHO 63-34795	Feb. 15, 1988	Japan			NO
	SHO 61-107453	May 26, 1986	Japan			NO
	SHO 63-91766	April 22, 1988	Japan			YES
	SHO 62-16289	Jan. 24, 1987	Japan			NO
TNT	SHO 61-160556	Oct. 4, 1986	Japan			NO

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
	<del>62-51509</del>	<del>Mar. 6, 1987</del>	<del>Japan</del>			<del>YES</del>

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


EXAMINER <i>TAN T. NGUYEN</i>	DATE CONSIDERED <i>25/1/01</i>
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